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Simulation of Nanoscale AlGaIn/GaN High -Electron Mobility Transistors Employing Field-Plate Technology

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The excellent microwave power performance demonstrated in AlGaIn/GaN HEMTs (high-electron mobility transistors) results from the combination of high current density with high voltage operation^[1], which benefits from the high sheet charge density in these hetero-structures (10^{13} cm^{-2}), the high carrier mobility ($1500 \text{ cm}^2/\text{Vs}$) and saturation velocity ($1.5 \times 10^7 \text{ cm/s}$) in the channel and the high breakdown voltage inherent in the GaN material. However, their reliability still limits their applications in today's electronic systems. The newly developed field-plated AlGaIn/GaN high electron mobility transistors show improved performance due to the electric field reduction in the device channel and surface modification^[2]. We report on two dimensional numerical simulations of gate-recessed and field-plated AlGaIn/GaN HEMTs where all the important device parameters have been defined, the insulator thickness under the field plate is also an important design parameter to attain higher breakdown voltage, thus an improvement of the performances of HEMT devices.

[1] Y. F. Wu, A. Saxler, M. Moore, R. P. Smith, S. Sheppard, P. M. Chavarkar, T. Wisleder, U. K. Mishra, and P. Parikh, IEEE Elect. Dev. Let. 117(2004) 25

[2] K. H. Cho, Y. S. Kim, J. Lim, Y. H. Choi, M. K. Ha, Sol.Stat. Elect. 405(2010) 54.